ABSTRACT

Analog processing sections (3a to 3b) generate base band analog signal from the received signal. A/D converting sections (4a to 4b) the outputs of the analog processing

5 sections into digital signals. Soft-decision output equalizers (11a to 11b) make soft decisions on the digital signals. A combining section (12) combines the results of the soft decisions to output a soft-decision value. An error correcting section (13) performs error correction

10 processing with respect to the soft-decision value.